

BL8531C

High Efficiency, Multi-Function Step-up DC/DC Controller

DESCRIPTION

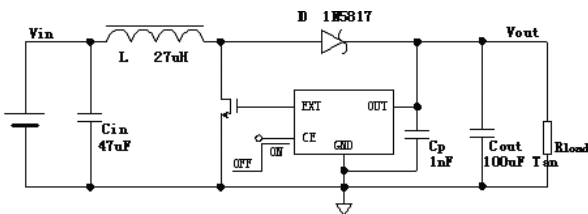
BL8531C series are CMOS-based PFM step-up DC-DC Controller with low supply current and high output voltage accuracy. Quiescent current drawn from power source is as low as 6 μ A. It is capable of delivering 500mA output current at 4.0V output with 2V input Voltage. Only four external components are necessary: An inductor, a Schottky diode, an output filter capacitor and a NMOSFET or a NPN transistor. All of these features make BL8531C series be suitable for the portable devices, which are supplied by a single battery to four-cell batteries.

BL8531C has a drive pin (EXT) for external transistor. So it is possible to load a large output current with a power transistor which has a low voltage dropout.

BL8531C integrates stable reference circuits and trimming technology, so it can afford high precision and low temperature-drift coefficient of the output voltage.

BL8531C is available in SOT-23-3, SOT-23-5 and SOT-89-3 packages which are PB free. And in SOT-23-5 the device can be switch on or off easily by CE pin, to minimize the standby supply current.

TYPICAL APPLICATION



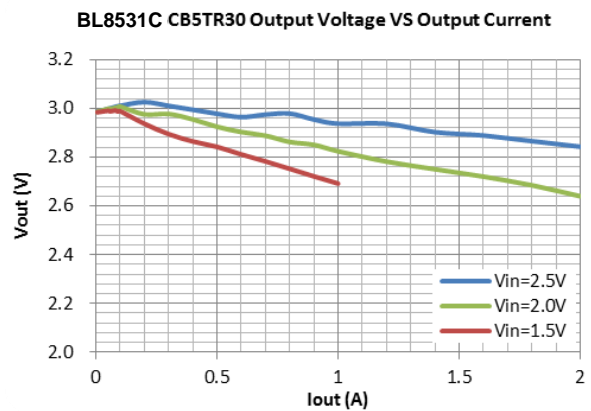
FEATURES

- Deliver 500mA at 4.0V Output voltage with 2V input Voltage
- The controller output voltage can be adjusted from 2.5V~6.0V(In 0.1V step)
- Output voltage accuracy ----- $\pm 2\%$
- Low temperature-drift coefficient of the output voltage----- $\pm 100\text{ppm}/^\circ\text{C}$
- Only four external components are necessary: An inductor, a Schottky diode, an output filter capacitor and a NMOSFET or a NPN transistor
- High power conversion efficiency-----90%
- Low quiescent current drawn from power source----- 6 μ A
- Small package--SOT-23-3, SOT-23-5, SOT-89-3

APPLICATIONS

- Power source for PDA, DSC, MP3 Player, electronic toy and wireless mouse
- Power source for a single or dual-cell battery-powered equipments
- Power source for LED

ELECTRICAL CHARACTERISTICS



BL8531C

ORDERING INFORMATION

BL8531C ① ② ③ ④

Code	Description
①	Temperature&RoHS: C: -40~85°C, Pb Free RoHS Std.
②	Package type: B3: SOT-23-3 B5: SOT-23-5 C3: SOT-89-3
③	Packing type: TR: Tape&Reel (Standard)
④	Output voltage: e.g. 25=2.5V 56=5.6V 60=6.0V

ABSOLUTE MAXIMUM RATING

Parameter	Value	
Output Voltage Range	-0.3V-12V	
EXT Pin Voltage	-0.3V-(VOUT+0.3)	
CE Pin Voltage	-0.3V-(VOUT+0.3)	
Operating Junction Temperature (Tj)	125°C	
Ambient Temperature (Ta)	-40°C -85°C	
Power Dissipation	SOT-23-3	250mW
	SOT-23-5	250mW
	SOT-89-3	500mW
Storage Temperature (Ts)	-40°C -150°C	
Lead Temperature & Time	260°C,10S	

Note:

Exceed these limits to damage to the device.
Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMENDED WORK CONDITIONS

Parameter	Value
Input Voltage Range	0.8V-Vout
Inductor	10-100uH
Input Capacitor	≥10uF
Output Capacitor	47-220uF
Ambient Temperature (Ta)	-40°C -85°C

Suggestion: Use tantalum type capacitor to reduce the ripple of the output voltage. Use 1nF filter ceramic type capacitor to connect OUT pin and GND pin. The filter capacitor is recommended as close as possible to OUT pin and GND pin.

MARKING INFORMATION

Product Classification		BL8531CCB5TR□ □
Marking		
3CXX	3C:Product Code XX: Output Voltage	
Product Classification		BL8531CCB3TR□ □
Marking		
3CXX	3C:Product Code XX: Output Voltage	
Product Classification		BL8531CCC3TR□ □
Marking		
LBXX LLBYW	LB: Product Code	
	XX: Output Voltage	
	LL: LOT NO.	
	B: FAB Code	
	YW: Date Code	
CE	Chip Enable (Active high)	
GND	Ground Pin	
OUT	Output Pin, Power supply for internal	
EXT	Switching Pin	
NC	No Connection	

ELECTRICAL CHARACTERISTICS

Default condition (unless otherwise provided): $V_{in}=0.6 \times V_{out}$, $I_{out}=10\text{mA}$, Temperature= 25°C , Use external circuit in test circuit list

SYMBOL	ITEM	TEST CONDITIONS	REFERENCE DATA			UNIT
			MIN	TYP.	MAX	
Vout	Output Voltage		2.45	2.5	2.55	V
			2.646	2.7	2.754	
			2.94	3.0	3.06	
			3.234	3.3	3.366	
			3.528	3.6	3.672	
			3.92	4.0	4.08	
			4.9	5.0	5.1	
			5.88	6.0	6.12	
Vin	Input Voltage				12	V
Iin	Input Current *(No load)	$I_{out}=0\text{mA}$, $V_{in}=V_{out} \times 0.6$		20	25	μA
IDD	Quiescent current *	No external component, $V_{out} = V_{out} \times 1.05$		6	15	μA
Istandby	Chip leakage current	VCE=0V			1	μA
VCEH	CE "H" threshold voltage	VCE: 0→2V	0.8			V
VCEL	CE "L" threshold voltage	VCE: 2→0V			0.3	V
Fosc	Oscillator frequency	$V_{out}=V_{out} \times 0.96$ Test EXT pin frequency		400		KHz
IEXTH	EXT"H" output current	$3.0\text{V} \leq V_{out} \leq 3.9\text{V}$		-21		mA
		$4.0\text{V} \leq V_{out} \leq 4.9\text{V}$		-35		mA
		$5.0\text{V} \leq V_{out} \leq 6.9\text{V}$		-41		mA
IEXTL	EXT"L" output current	$3.0\text{V} \leq V_{out} \leq 3.9\text{V}$		23		mA
		$4.0\text{V} \leq V_{out} \leq 4.9\text{V}$		25		mA
		$5.0\text{V} \leq V_{out} \leq 6.9\text{V}$		31		mA
Duty	Oscillator duty cycle	On(Vlx "L")side	70	75	80	%

Note:

Diode: Schottky type, such as: 1N5817, 1N5819, 1N5822

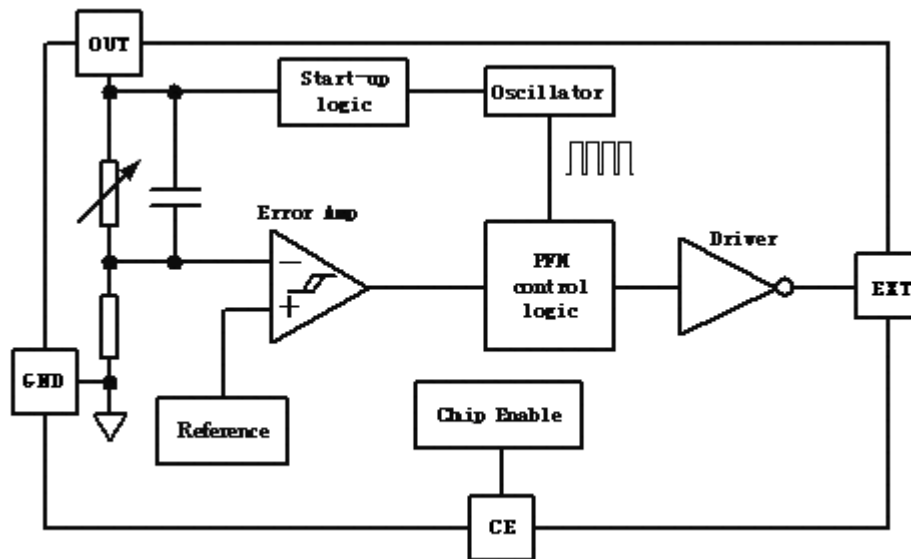
Inductor: 27 μH ($R < 0.5\Omega$)

Output capacitor: 100 μF (Tantalum type)

Vout pin filter capacitor: 1nF(Ceramic type)

Input capacitor: 47 μF

BLOCK DIAGRAM



TYPICAL APPLICATIONS

(1) Application with external NMOSFET

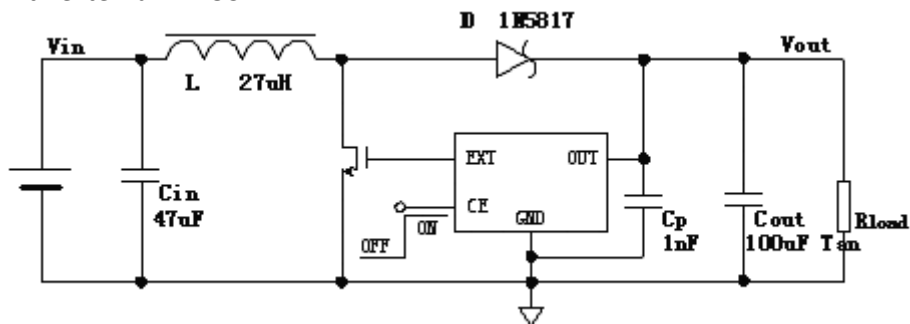


Fig 2

(2) Application with external NPN transistor

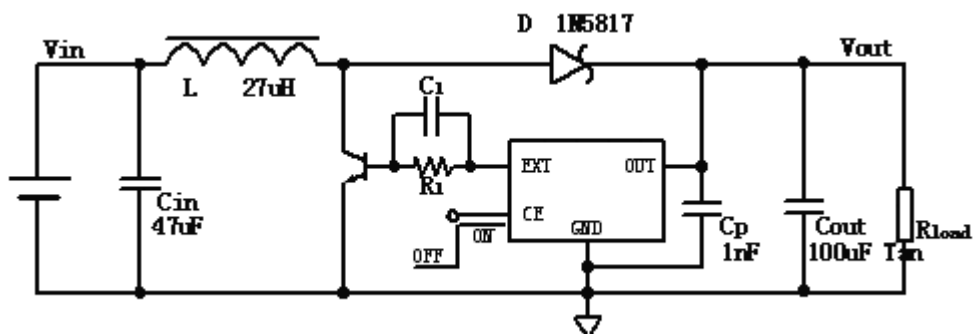


Fig 3

Note: $R1=330\Omega$, $C1=10nF$. ($R1$ can be calculated by load. If load is light $R1$'s value can be added. If load is heavy $R1$'s value can be smaller.)

DETAILED DESCRIPTION

BL8531C series are boost structure, voltage-type pulse-frequency modulation(PFM) step-up DC-DC controller. Only four external components are necessary: an inductor, a schottky diode, an output filter capacitor and a NMOSFET or a NPN transistor. The step-up DC-DC converter, constructed by BL8531C, can be adjusted from 2.5V to 6.0V, 0.1V step. By using the depletion technics, the quiescent current drawn from power source is lower than 8uA. The high efficiency device consists of resistors for output voltage detection and trimming, a start-up voltage circuit, an oscillator, a reference circuit, a PFM control circuit, a switch protection circuit and a driver transistor.

BL8531C integrates PFM control system. This system controls fixed power switch on duty cycle frequency to stabilize output voltage by calculating results of other blocks which sense input voltage, output voltage, output current and load conditions. In PFM modulation system, the frequency and pulse width is fixed. The duty cycle is adjusted by skipping pulses, so that switch on-time is changed based on the conditions such as input voltage, output current and load. The oscillate block inside BL8531C provides fixed frequency and pulse width wave.

The reference circuit provides stable reference voltage to output stable output voltage. Because internal trimming technology is used, the chip output change less than $\pm 2\%$. At the same time, the problem of temperature-drift coefficient of output voltage is considered in design, so temperature-drift coefficient of output voltage is less than 100ppm/ $^{\circ}\text{C}$.

High-gain differential error amplifier guarantees stable output voltage at difference input voltage and load. In order to reduce ripple and noise, the error amplifier is designed with high band-width.

BL8531C has a drive pin (EXT) for external transistor. So it is possible to load a large output current with a power transistor and a low saturation voltage. At very light load condition,

the switch current and quiescent current of chip will effect efficiency certainly. So in very light load condition, the efficiency will drop. Therefore, it is recommended that user use BL8531C in the condition of load current as large as several tens of mA to several hundreds of mA.

SELECTION OF THE EXTERNAL COMPONENTS

Thus it can be seen, the inductor, schottky diode and external NMOSFET or NPN transistor. affect the conversion efficiency greatly. The inductor and the capacitor also have great influence on the output voltage ripple of the converter. So it is necessary to choose a suitable inductor, a capacitor, an external NMOSFET or NPN transistor and a right schottky diode, to obtain high efficiency and low ripple.

Before discussion, we define

$$D \equiv \frac{V_{out} - V_{in}}{V_{out}}$$

INDUCTOR SELECTION

Above all, we should define the minimum value of the inductor that can ensure the boost DC-DC to operate in the continuous current-mode condition.

$$L_{\min} \geq \frac{D(1-D)^2 R_L}{2f}$$

The above expression is got under conditions of continuous current mode, neglect Schottky diode's voltage, ESR of both inductor and capacitor. The actual value is greater that it. If inductor's value is less than L_{\min} , the efficiency of DC-DC converter will drop greatly, and the DC-DC circuit will not be stable.

Secondly, consider the ripple of the output voltage,

$$\Delta I = \frac{D \cdot V_{in}}{L f}$$
$$I_{max} = \frac{V_{in}}{(1-D)^2 R_L} + \frac{D V_{in}}{2 L f}$$

If inductor value is too small, the current ripple through it will be great. Then the current through diode and power switch will be great. Because the power switch on chip is not ideal switch, the energy of switch will improve. The efficiency will fall.

Thirdly, in general, smaller inductor values supply more output current while larger values start up with lower input voltage and acquire high efficiency.

An inductor value of 3uH to 1mH works well in most applications. If DC-DC converter delivers large output current (for example: output current is great than 50mA), large inductor value is recommended in order to improve efficiency. If DC-DC must output very large current at low input supply voltage, small inductor value is recommended.

The ESR of inductor will effect efficiency greatly. Suppose ESR value of inductor is r_L , R_{load} is load resistor, then the energy can be calculated by following expression :

$$\Delta\eta \approx \frac{r_L}{R_{load}(1-D)^2}$$

For example: input 1.5V, output is 3.0V, $R_{load}=20\Omega$, $r_L=0.5\Omega$, The energy loss is 10%.

Consider all above, inductor value of 47uH、 $ESR<0.5\Omega$ is recommended in most applications. Large value is recommended in high efficiency applications and smaller value is recommended

OUTPUT CAPACITOR SELECTION

Ignore ESR of capacitor, the ripple of output voltage is:

$$r = \frac{\Delta V_{out}}{V_{out}} = \frac{D}{R_{load} C_f}$$

So large value capacitor is needed to reduce ripple. But too large capacitor value will slow down system reaction and cost will improve. So 100uF capacitor is recommended. Larger capacitor value will be used in large output current system. If output current is small (<10mA), small value is needed.

Consider ESR of capacitor, ripple will increase:

$$r' = r + \frac{I_{max} \cdot R_{ESR}}{V_{out}}$$

When current is large, ripple caused by ESR will be main factor. It may be greater than 100mV. The ESR will affects efficiency and increase energy loss. So low-ESR capacitor (for example: tantalum capacitor) is recommend or connect two or more filter capacitors in parallel.

DIODE SELECTION

Rectifier diode will affects efficiency greatly, Though a common diode (such as 1N4148) will work well for light load, it will reduce about 5%~10% efficiency for heavy load, For optimum performance, a Schottky diode (such as 1N5817,1N5819,1N5822) is recommended.

INPUT CAPACITOR

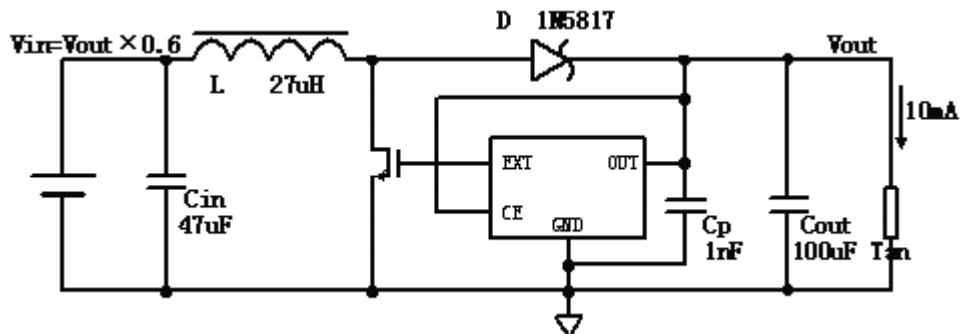
If supply voltage is stable, the DC-DC circuit can output low ripple, low noise and stable voltage without input capacitor. If voltage source is far away from DC-DC circuit, input capacitor value greater than 10uF is recommended.

Vout~GND FILTER CAPACITOR

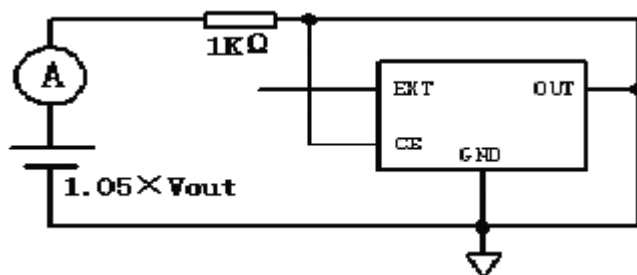
Because the chip's switch current flows from Vout pin, then through the chip into GND pin. Therefore if the output capacitor's two pins were not very near the chip's Vout pin and GND pin, Vout 's stable would be affected. User will found that the output voltage will drop when load grows up if the output capacitor's two pin is not very near the chip's Vout pin and GND pin. In this condition, 1nF ceramic capacitor is recommended at very near the chip's Vout pin and GND pin. So in all BL8531C application, two capacitors are needed to obtain stable output voltage. The 100uF tantalum output capacitor is recommended to stable output voltage nearby load. The 1nF Vout pin to GND pin ceramic filter capacitor is recommended to stable chip's sense voltage.

TEST CIRCUITS

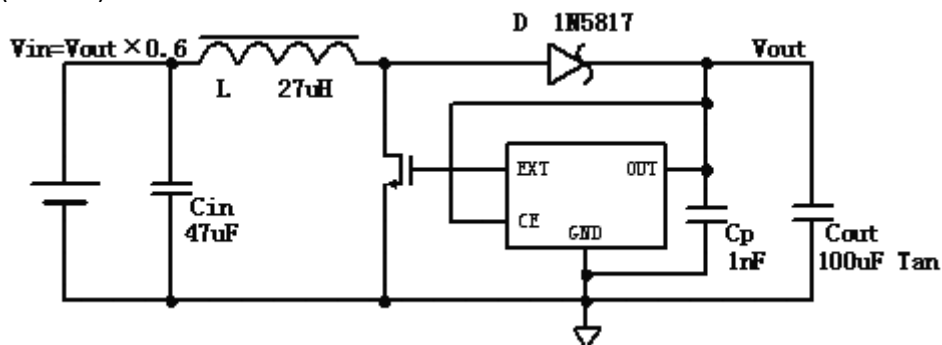
Output voltage test circuit



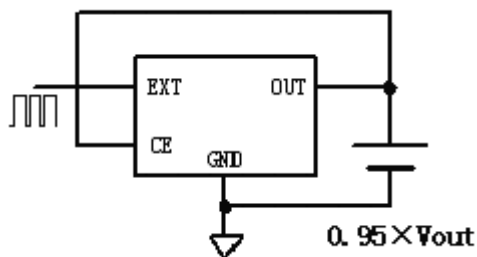
Quiescent current test circuit



Input Current (no load) test circuit



Oscillator frequency and duty cycle test circuit



EXTEND APPLICATIONS

12V STEP-UP APPLICATION

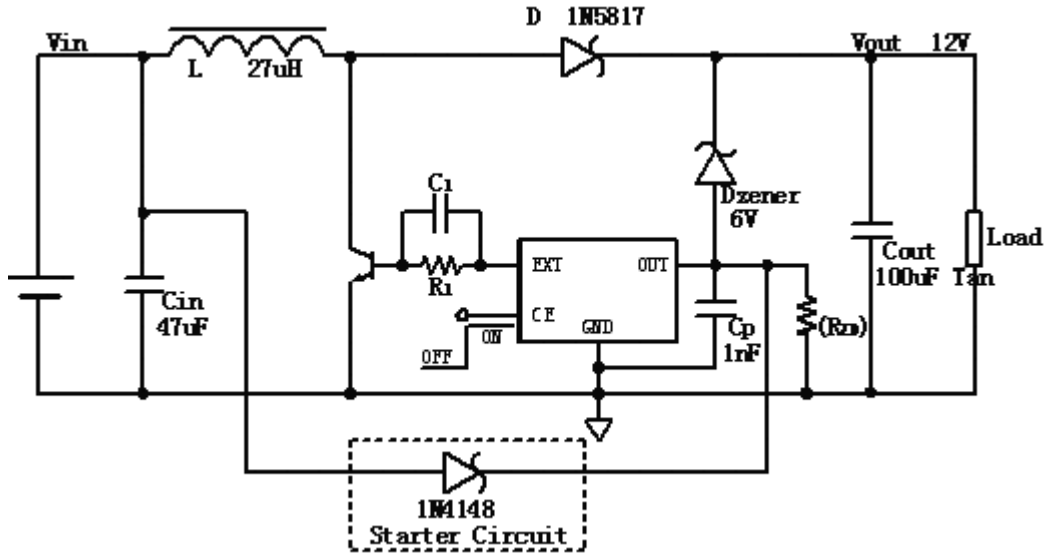


Fig 8

Note: BL8531C's output voltage is 6V. When the output current is small or no load, the output voltage will be unstable, use the RZD for flowing the bias current through the zener diode. For step-up application, a diode(for example: 1N4148) is needed as starter circuit.

STEP-DOWN APPLICATION

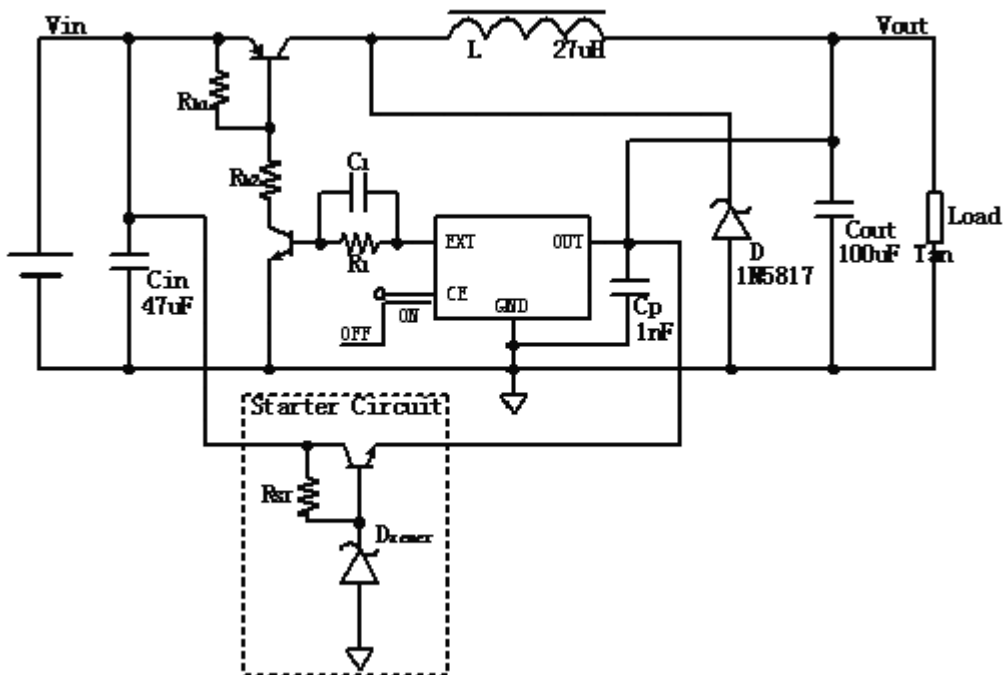


Fig 9

Note: In step-down application, use starter circuit as above. $2.5V \leq V_{zener} \leq V_{out}$. RZR is needed for bias current of zener diode. This starter circuit also can be used in high voltage step-up application.

FLYBACK STEP-UP/STEP-DOWN APPLICATION

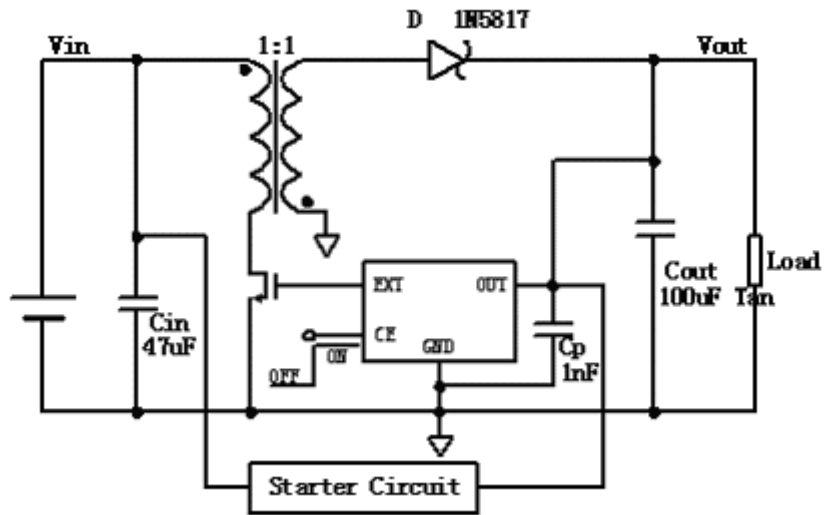


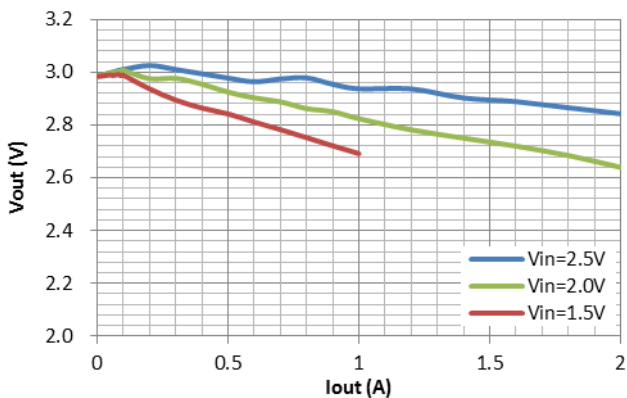
Fig 10

Note: In step-down and step-up/step-down application, starter circuit in fig 8 is need. In step-up application, simpler starter circuit in fig 9 can be used.

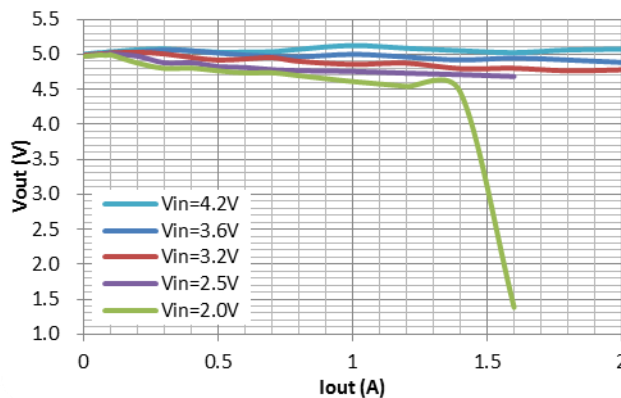
TYPICAL PERFORMANCE CHARACTERISTICS

(Recommended operating conditions: L=10uH, Cin=47uF, Cout=100uF, Cp=1nF Topt=25°C. unless otherwise noted)

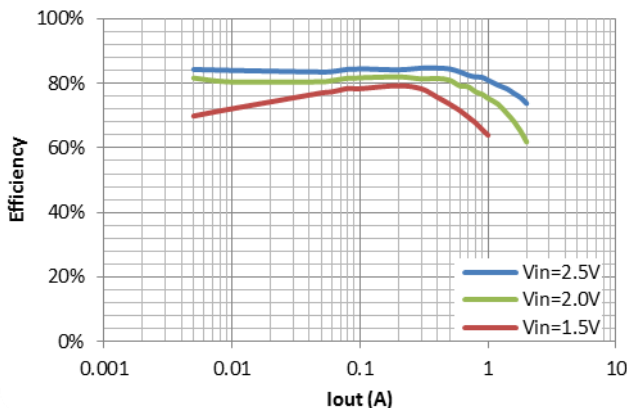
BL8531C CB5TR30 Output Voltage VS Output Current



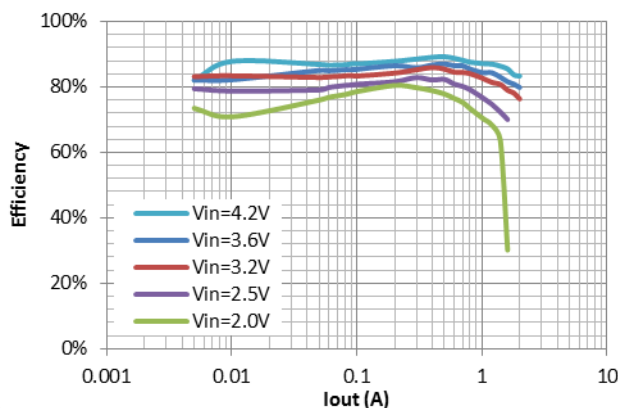
BL8531C CB5TR50 Output Voltage VS Output Current



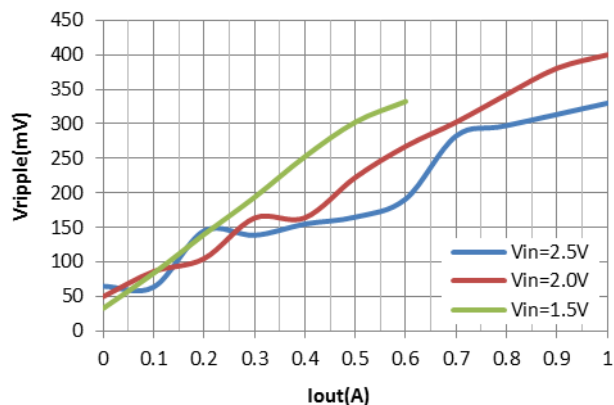
BL8531C CB5TR30 Efficiency VS Output Current



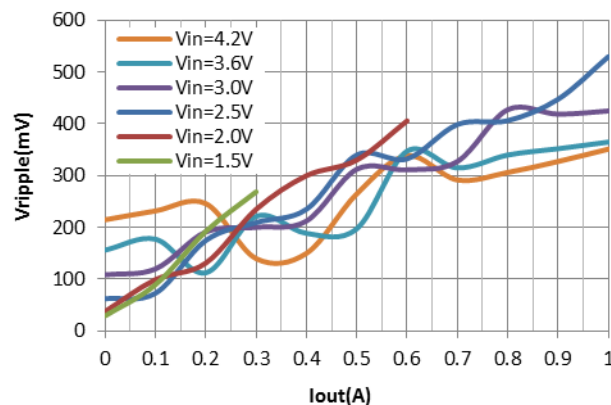
BL8531C CB5TR50 Efficiency VS Output Current



BL8531C CB5TR30 Ripple VS Output Current



BL8531C CB5TR50 Ripple VS Output Current

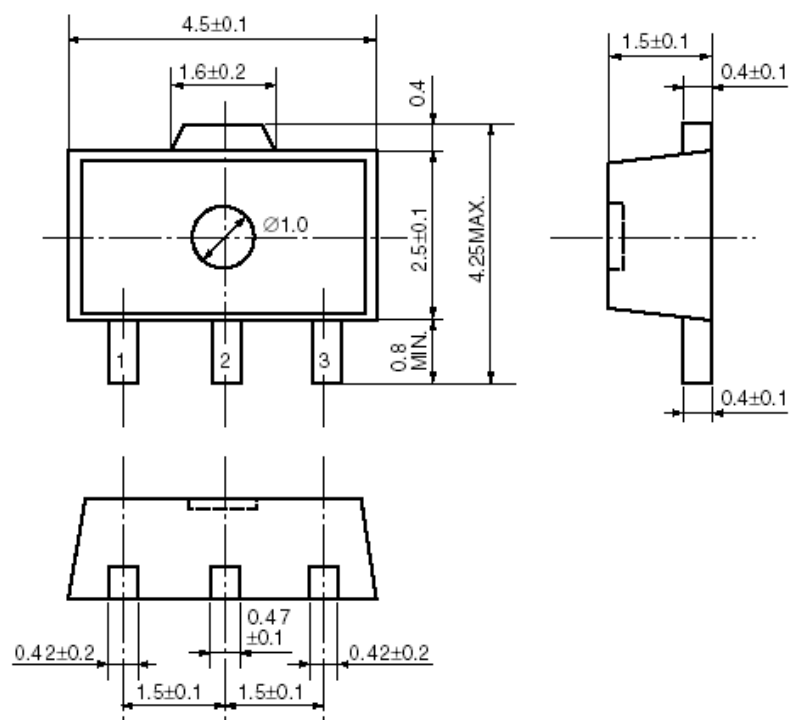


PACKAGE LINE

Package	SOT-23-3	Devices per reel	3000Pcs	Unit	mm
Package dimension:					
<p>Technical drawing of the SOT-23-3 package. The top view shows a rectangular body with a width of 2.9 ± 0.2 mm and a length of 1.9 ± 0.2 mm. The distance between the two leads is 1.9 ± 0.2 mm, with each lead being 0.95 mm wide. The lead width is 0.4 ± 0.1 mm. The body height is 1.6 ± 0.2 mm, and the total height including the lead is 2.8 ± 0.3 mm. The lead height is 0.8 mm. The side view shows a lead height of 1.1 mm with a tolerance of $+0.2$ to -0.1 mm, and a lead thickness of 0.16 mm with a tolerance of $+0.1$ to -0.06 mm. The lead angle is 0.2 mm minimum. The lead is bent at a 0 to 0.1 mm angle. The maximum lead length is 1.4 mm.</p>					

Package	SOT-23-5	Devices per reel	3000Pcs	Unit	mm
Package specification:					
<p>Technical drawing of the SOT-23-5 package. The top view shows a rectangular body with a width of 2.9 ± 0.2 mm and a length of 1.9 ± 0.2 mm. The distance between the two leads is 1.9 ± 0.2 mm, with each lead being 0.95 mm wide. The lead width is 0.4 ± 0.1 mm. The body height is 1.6 mm with a tolerance of $+0.2$ to -0.1 mm, and the total height including the lead is 2.8 ± 0.3 mm. The lead height is 0.8 ± 0.1 mm. The side view shows a lead height of 1.1 mm with a tolerance of $+0.2$ to -0.1 mm, and a lead thickness of 0.15 mm with a tolerance of $+0.1$ to -0.05 mm. The lead angle is 0.2 mm minimum. The lead is bent at a 0 to 0.1 mm angle.</p>					

BL8531C

Package	SOT-89-3	Devices per reel	1000Pcs	Unit	mm
Package Dimension:					
 <p>The drawing illustrates the mechanical dimensions of the BL8531C SOT-89-3 package. It includes three views: a top view, a side view, and a bottom view. The top view shows a rectangular body with a central circular feature of diameter $\varnothing 1.0$. The overall width is 4.5 ± 0.1 mm, and the length is 2.5 ± 0.1 mm. The maximum height of the package is 4.25 mm. Three leads are shown, labeled 1, 2, and 3, with a minimum height of 0.8 mm. The lead spacing between leads 1 and 2, and between 2 and 3, is 1.5 ± 0.1 mm. The lead width is 0.4 ± 0.1 mm. The top view also shows a width of 1.6 ± 0.2 mm for the central feature and a height of 0.4 mm for the top flange. The side view shows the profile of the package with a width of 1.5 ± 0.1 mm and a lead height of 0.4 ± 0.1 mm. The bottom view shows the lead layout with a lead width of 0.42 ± 0.2 mm and a lead spacing of 1.5 ± 0.1 mm. A central feature on the bottom view has a width of 0.47 ± 0.1 mm.</p>					