

Positive Voltage Regulators

■ GENERAL DESCRIPTION

The XC6201 series are highly precise, low power consumption, positive voltage regulators manufactured using CMOS and laser trimming technologies.

The series provides large currents with a significantly small dropout voltage.

The XC6201 consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error amplifier. Output voltage is selectable in 0.1V steps between $1.3V \sim 6.0V$.

SOT-25, SOT-89 and USP-6B packages are available.

■ APPLICATIONS

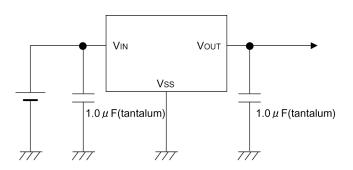
- Smart phones / Mobile phones
- Portable game consoles
- Digital still cameras / Camcorders
- Digital audio equipment
- Reference voltage sources
- Multi-function power supplies

FEATURES

Maximum Output Current	: 250mA (TYP.)
Dropout Voltage	: 0.16V @ 100mA
	: 0.40V @ 200mA
Maximum Operating Voltage	: 10V
Output Voltage Range	: 1.3V ~ 6.0V (0.1V increments)
Fixed Voltage Accuracy	: ±1% (Vouт(т) <u>≥</u> 2.0V)
	±2%
Low Power Consumption	: 2.0 µ A (TYP.)
Operating Ambient Temperature	: -40°C ~ 85°C
Packages	: SOT-25,
	SOT-89
	USP-6B
Environmentally Friendly	: EU RoHS Compliant, Pb Free

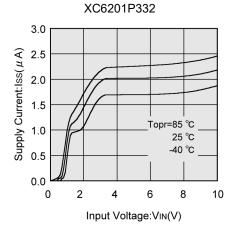
Tantalum or Ceramic Capacitor compatible

■ TYPICAL APPLICATION CIRCUIT

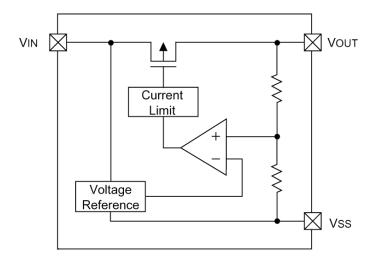


■ TYPICAL PERFORMANCE CHARACTERISTICS

Supply Current vs. Input Voltage



■BLOCK DIAGRAM



■ PRODUCT CLASSIFICATION

Ordering Information

 $X C 6 2 0 1 P 34567 - 8^{(*1)}$

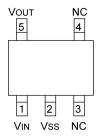
1 1 1 2

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
1	Product Number	01	-
2	Type of Regulator	Р	3-pin regulator
34	Output Voltage	13 ~ 60	e.g. 30:3.0V 50:5.0V
5	Output Voltage Accuracy	1	±1% ±2%
	Daakagaa	MR-G	SOT-25 (3,000pcs/Reel)
67-8	Packages (Order Unit)	PR-G	SOT-89 (1,000pcs/Reel)
		DR-G	USP-6B (3,000pcs/Reel)

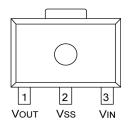
(*1) The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.

* \pm 1% accuracy can be set at V_{OUT(T)} \geq 2.0V.

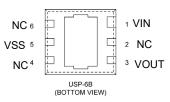
■ PIN CONFIGURATION



SOT-25 (TOP VIEW)



SOT-89 (TOP VIEW)



*The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release. If the pad needs to be connected to other pins, it should be connected to the VSS (No.5) pin.

■ PIN ASSIGNMENT

	PIN NUMBER		PIN NAME	FUNCTION
SOT-25	SOT-89	USP-6B		FUNCTION
5	1	3	Vout	Output
2	2	5	Vss	Ground
1	3	1	Vin	Power Input
3, 4	-	2,4,6	NC	No Connection

■ABSOLUTE MAXIMUM RATINGS

				Ta = 25°C		
PARAM	1ETER	SYMBOL RATINGS		UNITS		
Input V	oltage	VIN	12.0	V		
Output (Current	l _{оит}	500	mA		
Output V	Voltage	Vout	V _{SS} -0.3 ~ V _{IN} +0.3	V		
	SOT-25		250			
	301-23		760 (JESD51-7 board) ^(*1)			
Power	SOT-89		Dd	Pd	500	mW
Dissipation	301-69	Fu	1000 (40mm x 40mm Standard board) ^(*1)	11177		
	USP-6B		120			
	03F-0B		1000 (40mm x 40mm Standard board) ^(*1)			
Operating Te	emperature	Topr	-40 ~ 85	C°		
Storage Te	Storage Temperature		-55 ~ 125	C°		

Each voltage rating is based on Vss.

^(*1) The power dissipation figure shown is PCB mounted and is for reference only. Please refer to PACKAGING INFORMATION for the mounting condition.

■ ELECTRICAL CHARACTERISTICS

XC6201P132 Vout(t)=1.3	BV ^(*1)						Ta=25°C
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT(E)} ^(*2)	V _{IN} =2.3V Iou⊤=10mA	1.274	1.300	1.326	V	2
Maximum Output Current	I _{OUTmax}	V _{IN} =2.3V V _{OUT(E)} ≧1.17V	60	-	-	mA	2
Load Regulation	ΔVout	V _{IN} =2.3V 1mA≦I _{OUT} ≦30mA	-	10	30	mV	2
Dropout Voltage (*3)	Vdif1	Iout=30mA	-	200	600	mV	2
Dropout voltage ()	Vdif2	Ιουτ=60mA	-	500	810	mv	Z
Supply Current	lss	V _{IN} =2.3V	-	2.0	5.0	μA	1
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot \Delta V_{OUT}}$	I _{OUT} =10mA 2.3V≦V _{IN} ≦10.0V	-	0.2	0.3	%/V	2
Input Voltage	Vin		1.8	-	10	V	-
Output Voltage Temperature Characteristics	$\frac{\Delta V_{OUT}}{\Delta \text{Topr} \cdot \Delta V_{OUT}}$	l _{ou⊤} =40mA -40°C≦Topr≦85°C	-	±100	-	ppm/°C	2

XC6201P182 Vout(t)=1.8	3V ^(*1)						Ta=25°C
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	Vout(e) ^(*2)	V _{IN} =2.8V IOUT=40mA	1.764	1.800	1.836	V	2
Maximum Output Current	I _{OUTmax}	V _{IN} =2.8V V _{OUT(E)} ≧1.62V	80	-	-	mA	2
Load Regulation	ΔVout	V _{IN} =2.8V 1mA≦I _{OUT} ≦40mA	-	10	30	mV	2
	Vdif1	Iout=40mA	-	200	370	m)/	2
Dropout Voltage ^(*3)	Vdif2	Iout=80mA	-	450	710	mV	(2)
Supply Current	lss	V _{IN} =2.8V	-	2.0	5.0	μA	1
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot \Delta V_{OUT}}$	I _{OUT} =40mA 2.8V≦V _{IN} ≦10.0V	-	0.2	0.3	%/V	2
Input Voltage	VIN		1.8	-	10	V	-
Output Voltage Temperature Characteristics	$\frac{\Delta V_{OUT}}{\Delta \text{Topr} \cdot \Delta V_{OUT}}$	l _{oυ⊤} =40mA -40°C≦Topr≦85°C	-	±100	-	ppm/°C	2

Vout(t)=2.7V (*1) XC6201P272

Ta=25℃

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT(E)} ^(*2)	V _{IN} =3.7V I _{OUT} =40mA	2.646	2.700	2.754	V	2
Maximum Output Current	I _{OUTmax}	V _{IN} =3.7V V _{OUT(E)} ≧2.43V	100	-	-	mA	2
Load Regulation	ΔVout	V _{IN} =3.7V 1mA≦I _{OUT} ≦60mA	-	15	40	mV	2
Dropout Voltage (*3)	Vdif1	Iоит =60mA	-	200	370	m)/	2
Diopout voltage	Vdif2	І _{оυт} =120mA	-	450	710	mV	Q
Supply Current	lss	V _{IN} =3.7V	-	2.0	5.0	μA	1
Line Regulation	 ΔV _{IN} ·ΔV _{OUT}	I _{OUT} =40mA 3.7V≦V _{IN} ≦10.0V	-	0.2	0.3	%/V	2
Input Voltage	VIN		1.8	-	10	V	-
Output Voltage Temperature Characteristics	$\frac{\Delta V_{\text{OUT}}}{\Delta \text{Topr} \cdot \Delta V_{\text{OUT}}}$	l _{ou⊤} =40mA -40°C≦Topr≦85°C	-	±100	-	ppm/°C	2

■ ELECTRICAL CHARACTERISTICS (Continued)

XC6201P332 V _{OUT(T)} =3.	3V ^(*1)						Ta=25°C
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	Vout(e) ^(*2)	V _{IN} =4.3V I _{OUT} =40mA	3.234	3.300	3.366	V	2
Maximum Output Current	IOUTmax	V _{IN} =4.3V V _{OUT(E)} ≧2.97V	150	-	-	mA	2
Load Regulation	ΔV _{OUT}	V _{IN} =4.3V 1mA≦I _{OUT} ≦80mA	-	20	50	mV	2
Dropout Voltage (*3)	Vdif1	Ιουτ =80mA	-	200	360	mV	2
Diopout voltage	Vdif2	Iоит=160mA	-	450	700	IIIV	2
Supply Current	I _{SS}	V _{IN} =4.3V	-	2.0	5.0	μA	1
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot \Delta V_{OUT}}$	I _{OUT} =40mA 4.3V≦V _{IN} ≦10.0V	-	0.2	0.3	%/V	2
Input Voltage	V _{IN}		1.8	-	10	V	-
Output Voltage Temperature Characteristics	 Δ Τορr ∙ΔV _{Ουτ}	l _{ou⊤} =40mA -40°C≦Topr≦85°C	-	±100	-	ppm/°C	2

XC6201P502 V _{OUT(T)} =5.	0V ^(*1)						Ta=25°C
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Output Voltage	V _{OUT(E)} ^(*2)	V _{IN} =6.0V	4.900	5.000	5.100	V	2
	•001(L)	lou⊤=40mA	1.000	0.000	0.100	v	Ú
Maximum Output Current	lour	VI _N =6.0V	200			mA	2
Maximum Output Current	IOUTmax	V _{OUT(E)} ≧4.57V	200	-	-		Z
Load Population	ΔVout	V _{IN} =6.0V		30	70	mV	2
Load Regulation		1mA≦I _{OUT} ≦100mA	-	30			Ľ
Dropout Voltage (*3)	Vdif1	I _{ОUT} =100mA	-	160	340	mV	2
Diopout voltage	Vdif2	I _{0UT} =200mA	-	400	600	IIIV	Ľ
Supply Current	lss	V _{IN} =6.0V	-	2.0	6.0	μA	1
Line Regulation	ΔVout	lou⊤=40mA		0.2	0.3	%/V	2
	$\Delta V_{IN} \cdot \Delta V_{OUT}$	6.0V≦V _{IN} ≦10.0V	-	0.2	0.5	%0/V	Ľ
Input Voltage	VIN		1.8	-	10	V	-
Output Voltage	ΔVουτ	louτ=40mA		±100		nnm/°C	0
Temperature Characteristics	ΔTopr·ΔV _{OUT}	-40°C≦Topr≦85°C	-	± 100	-	ppm/°C	2

NOTE:

*1: $V_{OUT(T)}$ = Nominal output voltage.

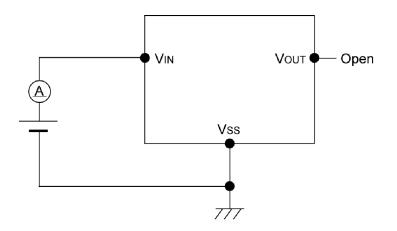
*2: V_{OUT(E)} = Effective output voltage (i.e. the output voltage when "V_{OUT(T)}+1.0V" is provided while maintaining a certain I_{OUT} value).

*3: Vdif = (V_{IN1}- V_{OUT1})

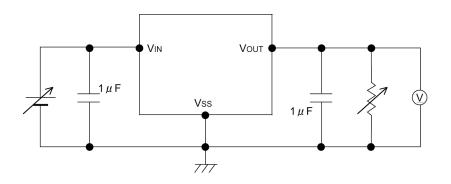
 $\begin{array}{l} \text{VIN1 :} \text{VIN1 :} \text{An Input Voltage when } V_{\text{OUT1}} \text{ appears as the input voltage is gradually decreased.} \\ V_{\text{OUT1}} \text{ : } \text{A voltage equal to 98\% of the output voltage when a stabilized } (V_{\text{OUT(T)}} + 1.0V) \text{ is input.} \end{array}$

■TEST CIRCUITS

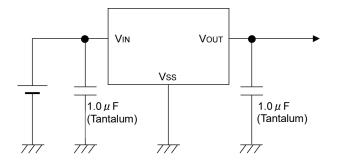
Circuit 1 : Supply Current



Circuit O : Output Voltage, Oscillation, Line Regulation, Dropout Voltage, Load Regulation

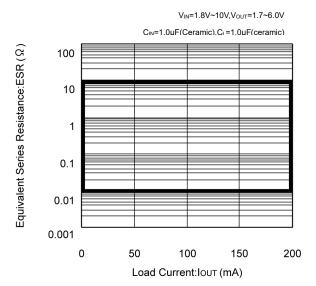


■ OPERATIONAL EXPLANATION



With the XC6201 series regulator, in order to ensure the stabilized output voltage, we suggest that an output capacitor (C_L) of 1 μ F or more be connected between the output pin (V_{OUT}) and the V_{SS} pin. For using low ESR capacitor (e.g. ceramic capacitors), please make sure that the output voltage is more than 1.7V. When the output voltage is from 1.3V to 1.6V, the output capacitor should be a tantalum capacitor with a capacitance of 2.2 μ F. We also suggest an input capacitor (C_{IN}) should be connected between the V_{IN} and the V_{SS} in order to stabilize input power source.

OUTPUT VOLTAGE	Cin	CL (TANTALUM)	CL (LOW ESR)
1.3V ~ 1.6V	≧1.0 µ F	≧2.2µF	-
1.7V ~ 6.0V	≧1.0 µ F	≧1.0 µ F	≧1.0 µ F



NOTE ON USE

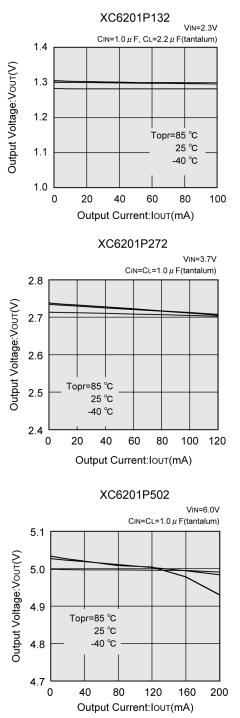
1. Please use this IC within the stated absolute maximum ratings. The IC is liable to malfunction should the ratings be exceeded. When a voltage higher than the V_{IN} flows to the V_{OUT} like when using two power supplies, please connect a Schottky barrier diode between the V_{OUT} and the V_{IN} and do not exceed the V_{OUT} rating.

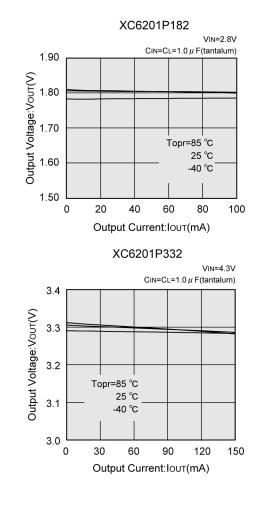
2. An oscillation may occur by the impedance between a power supply and the input of the IC. Where the impedance is 10Ω or more, please use an input capacitor (C_{IN}) of at least 1μ F. In case of high output current, operation can be stabilized by increasing the input capacitor value. Also an oscillation may occur if the input capacitor value is smaller than the input impedance when the output capacitance (C_L) is large. In such cases, operations can be stabilized by either increasing the input capacitor value.

3. Please ensure that output current (I_{OUT}) is less than Pd / ($V_{IN} - V_{OUT}$) and do not exceed the rated power dissipation value (Pd) of the package.

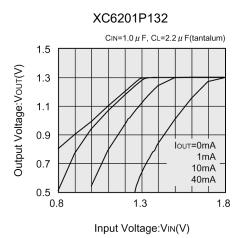
■ TYPICAL PERFORMANCE CHARACTERISTICS

(1) Output Voltage vs. Output Current

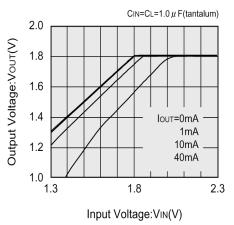


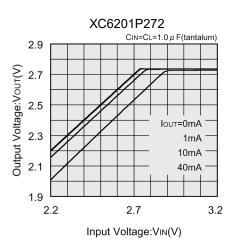


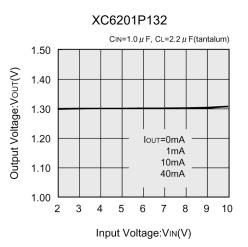
(2) Output Voltage vs. Input Voltage



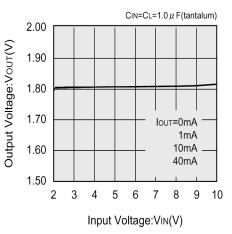




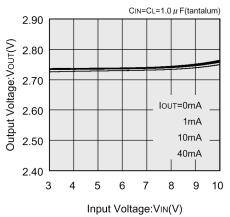




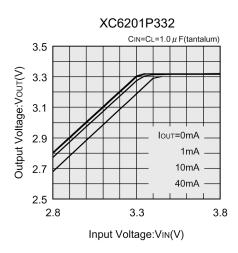
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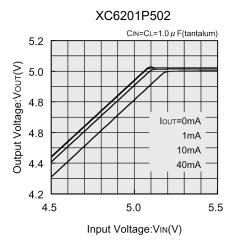


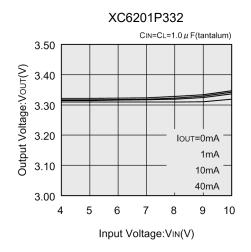




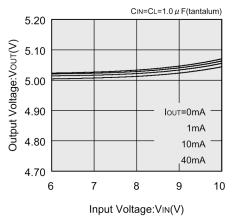
(2) Output Voltage vs. Input Voltage (Continued)



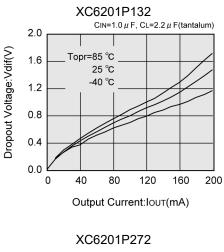


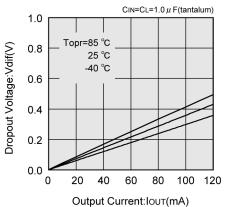




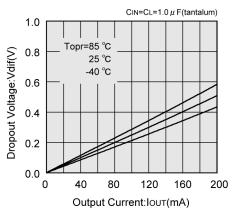


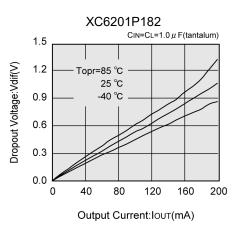
(3) Dropout Voltage vs. Output Current

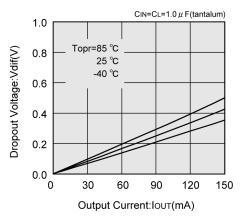




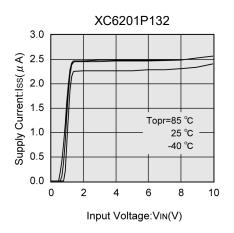


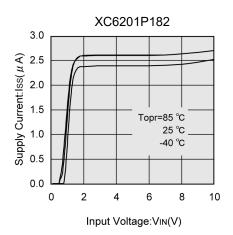




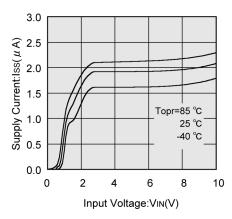


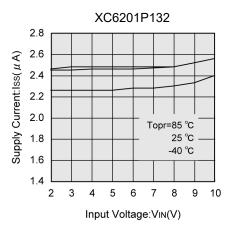
(4) Supply Current vs. Input Voltage

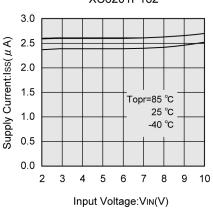


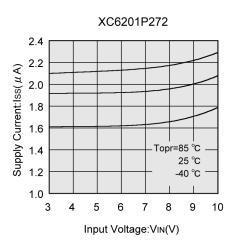




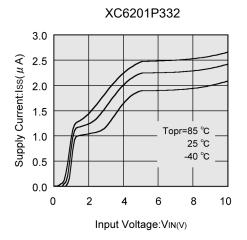




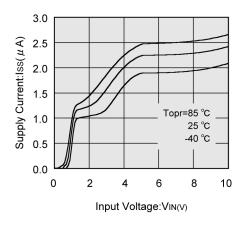




(4) Supply Current vs. Input Voltage (Continued)

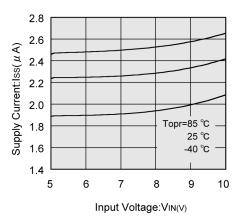


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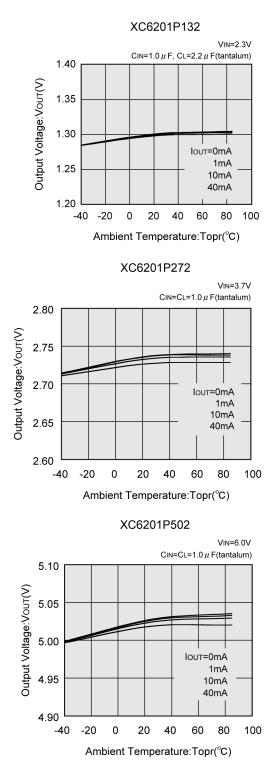


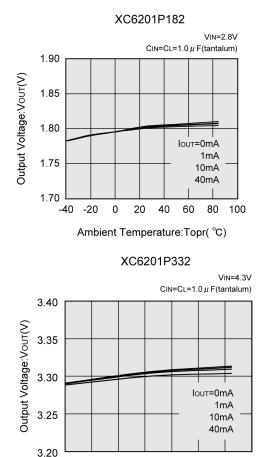
2.6 2.4 Supply Current:Iss(μ A) 2.2 2.0 1.8 1.6 Topr=85 °C 25 °C 1.4 -40 °C 1.2 5 6 8 9 10 4 7 Input Voltage:VIN(V)

XC6201P502



(5) Output Voltage vs. Ambient Temperature





40 Ambient Temperature:Topr(°C)

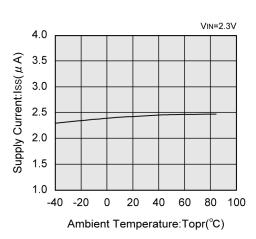
60 80 100

20

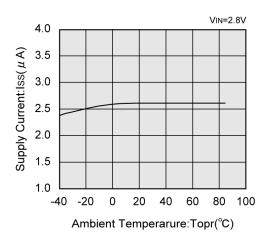
-40 -20 0

■TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

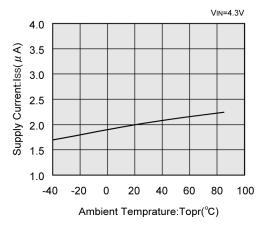
(6) Supply Current vs. Ambient Temperature

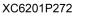


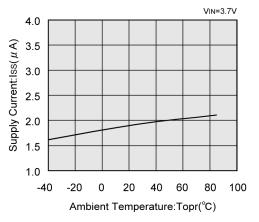
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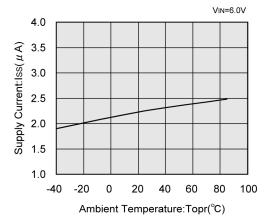
XC6201P332



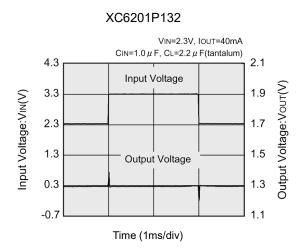




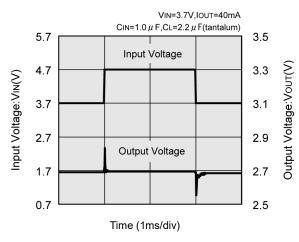




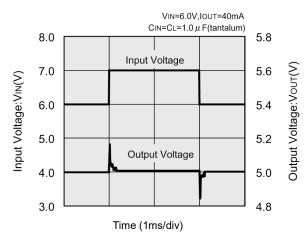
(7) Input Transient Response

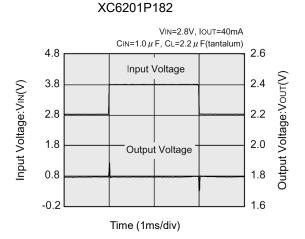




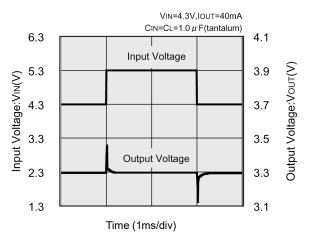




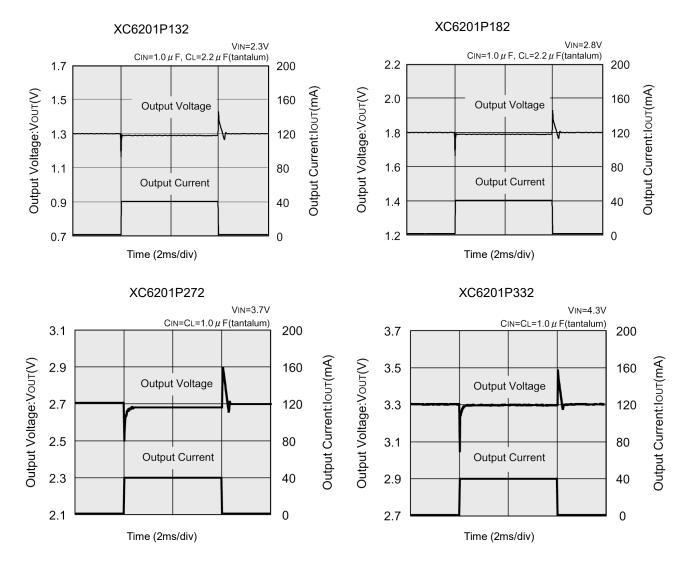


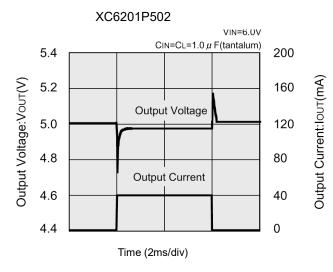




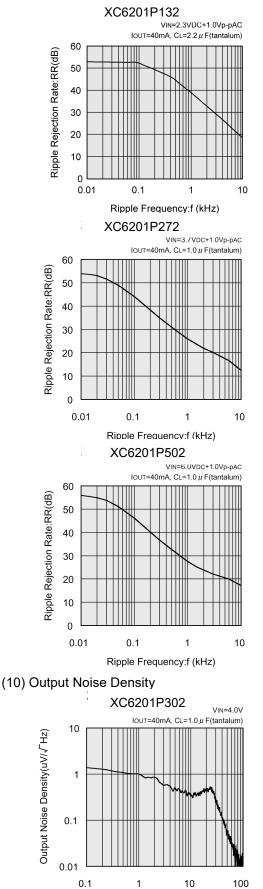


(8) Load Transient Response

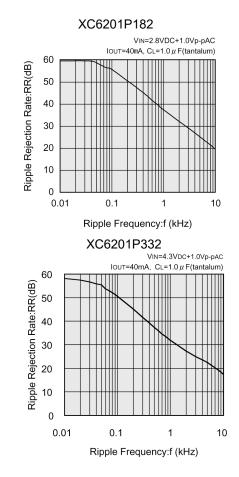




(9) Ripple Rejection Rate



Frequency:f(kHz)



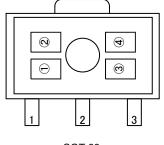
■PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS	
SOT-25 SOT-25 PKG SOT-25 Power Dissipation		SOT-25 Power Dissipation	
SOT-89 SOT-89 PKG SOT-89 Power Dissipation		SOT-89 Power Dissipation	
USP-6B USP-6B PKG		USP-6B Power Dissipation	

MARKING RULE

●SOT-89, SOT-25



SOT-89 (TOP VIEW)

5		4
1	23	4
1	2	3

SOT-25 (TOP VIEW)

1	represents	the p	product se	eries
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MARK	PRODUCT SERIES
1	XC6201xxxxxx

② represents type of regulator

MA	RK	PRODUCT SERIES
Voltage= 0.1 ~ 3.0V	Voltage= 3.1 ~ 6.0V	FRODUCT SERIES
5	6	XC6201Pxxxxx
8	9	XC6201TxxxPx

3 represents output voltage

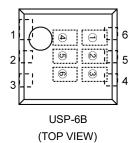
MARK	OUTPUT VOLTAGE (V)		MARK	OUTPUT VOLTAGE (V)		GE (V)	
0	-	3.1	_	F	1.6	4.6	-
1	—	3.2	_	Н	1.7	4.7	_
2	—	3.3	1	К	1.8	4.8	-
3	—	3.4	Ι	L	1.9	4.9	-
4	—	3.5	Ι	М	2.0	5.0	—
5	—	3.6	1	Ν	2.1	5.1	-
6	—	3.7	1	Р	2.2	5.2	-
7	—	3.8	Ι	R	2.3	5.3	—
8	—	3.9	Ι	S	2.4	5.4	—
9	—	4.0	1	Т	2.5	5.5	-
А	—	4.1	1	U	2.6	5.6	-
В	—	4.2	-	V	2.7	5.7	-
С	1.3	4.3	_	Х	2.8	5.8	—
D	1.4	4.4	_	Y	2.9	5.9	_
E	1.5	4.5	_	Z	3.0	6.0	—

④ represents assembly lot number

0 to 9, A to Z repeated (G, I, J, O, Q, W excluded)

■MARKING RULE (Continued)

●USP-6B



1 represents product series

③ represents type of regulator

MARK	TYPE	PRODUCT SERIES		
Р	3pin Regulator	XC6201PxxxDx		
T VIN=7V(Rated)		XC6201TxxxDx		

(4)(5) represents output voltage

MA	RK		PRODUCT SERIES	
4	5	VOLTAGE (V)		
3	3	3.3	XC6201x33xDx	
5	0	5.0	XC6201x50xDx	

⑥ represents assembly lot number

0 to 9, A to Z repeated (G, I, J, O, Q, W excluded) Note: No character inversion used.

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