Power Distribution Switch

Features

- 70mΩ High-Side MOSFET (G524A,G524B,G524C,G524D)
- Available with 4 Versions of Current Limits with Foldback
- Operating Range:2.7V to 5.5V
- 1mS Typical Rise Time
- Fast Overcurrent Response -1.5µs (TYPICAL)
- Under voltage Lockout
- 130µA Quiescent Supply Current
- 1µA Maximum Shutdown Supply Current
- Logic Level Enable Pin, Available with Active-High or Active-Low Version
- No Reverse Current when Power Off
- Deglitched Open-Drain Over-Current Flag Output (OC)
- Available with or without Output Shutdown Pull-low Resister
- Output Reverse-Voltage Protection
- SOT-23-5, TSOT-23-6, MSOP-8 and MSOP-8 (FD) Packages
- UL Certification_#E232223
- CB Test Certification by IEC 60950-1:2005 (2nd Edition); Am 1:2009

Applications

- High-Side Power Protection Switch
- USB Power Management
- USB Host and Self-Powered Bubs
- USB Bus-Powered Hubs
- Hot Plug-In Power Supplies
- Battery-Charger Circuits

General Description

The G524 is an integrated power switch for self-powered and bus-powered Universal Serial Bus (USB) applications. G524A, G524B, G524C, G524D are $70m\Omega$ $R_{\text{DS}(\text{ON})}.$

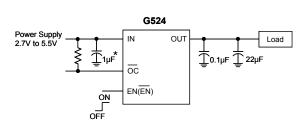
Several Protection features include current limiting and thermal shutdown to prevent catastrophic switch failure caused by increasing power dissipation when continuous heavy loads or short circuit occurs. A built-in charge pump is used to drive the N-channel MOSFET that is free of parasitic body diode to eliminate any reversed current flow across the switch when it is powered off. When the output voltage is higher than input voltage, the power switch is turned off by internal output reverse-voltage comparator.

 $\overline{\text{OC}}$ is an open-drain output report over-current or over-temperature event and has typical 9ms deglitch timeout period. In addition, $\overline{\text{OC}}$ also reports output reverse-voltage condition with typical 5ms deglitch timeout period.

Pin Configuration

G524 OUT 1 5 IN EN (EN) 1 0 6 <u>oc</u> 5 OUT GND 2 GND 2 oc 3 4 EN (EN) 4 OUT TSOT-23-6 SOT-23-5 G524 G524 8 OUT 8 OUT GND 1 0 GND 1 \circ 7 OUT 7 OUT IN 2 IN 2 Pad 6 OUT* 6 OUT* IN 3 IN 3 5 OC EN (EN) 4 5 OC EN (EN) 4 MSOP-8 MSOP-8 (FD) Note: Recommend connecting the Thermal Pad to the GND for excellent power dissipation.

Typical Application Circuit



^{*: 1}µF of input capacitor is enough in most application cases.

If the PCB trace of power rail to IN is long, larger input capacitor is necessary.

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^{*}Pin#6 should be considered as OUT when circuit design and PCB layout, but it is NC pin actually.